What is claimed is:

1. A method for making a plurality of surface mount resistors comprising:

taking a resistive strip of electrically resistive material having an upper edge, a lower edge, a central portion between said upper and lower edges, a front flat surface and a rear flat surface;

taking a single conductive strip having an upper edge, a lower edge, a central portion between said upper edge and said lower edge, a front flat surface and a rear flat surface;

attaching said rear flat surface of said single conductive strip in complete covering relation over said front flat surface of said resistive strip;

modifying said overlying strip by removing said central portion of said single conductive strip to expose said central portion of said resistive strip whereby said modified overlying strip comprises an upper conductive strip and a lower conductive strip overlying spaced apart upper and lower portions of said front flat face of said resistive strip, respectively, said upper and lower conductive strips being separated from one another and being connected by said central portion of said resistance strip;

sectioning said overlying strip into a plurality of body members, each of said body members comprising an upper conductive section of said upper strip and a lower conductive section of said lower strip joined by a central resistive section of said exposed central portion of said resistance strip;

encapsulating said exposed central resistive section of each of said resistive strips with an electrically insulating material.

2. A method according to claim 1 and further comprising attaching a carrier strip to overlying strip, said sectioning step being done so as to leave said carrier strip interconnecting said plurality of body members.

3. A method according to claim 2 and further comprising removing said plurality of body members from said carrier strip after said step of applying said encapsulating material.

4. A method according to claim 1 wherein said step of removing said central portion of said single conductive strip is done by a process selected from the group consisting essentially of grinding, milling or skiving.

5. A method of forming a surface mount resistor comprising:

taking a resistance strip, an upper conductive strip, and a lower conductive strip, each having an upper edge, a lower edge, a front flat surface and a rear surface;

attaching said rear surfaces of said upper and lower conductive strips to said front flat surface of said resistance strip in spaced parallel relationship to one another thereby leaving an exposed central portion of said resistance strip between and interconnecting said spaced apart upper and lower conductive strips;

applying an electrically insulating encapsulating material to said resistance strip so as to encapsulate said resistance strip within said encapsulating material between the upper and lower conductive strips.

6. The method of claim 5 wherein the step of attaching said upper and lower conductive strips to said resistance strip comprises attaching a single conductive strip in complete covering relation over said flat front surface of said resistive strip and removing a portion of said single conductive strip to create said spaced apart upper and lower conductive strips and to expose said central portion of said resistive strip.